JUN -2 8 2002

AP118HO

IN THE UNITED S TES PATENT AND TRADEMARK OFFICE

Inventor:

Changick Kim

Serial No.:

10/037,767

Filed:

October 23, 2001

Title:

Group Art Unit: 2624

Examiner: Not Yet Assigned PECEIVEL

Technology Conter 2600 Reducing Blocking And Ringing Artifacts In Low-Bit-Rate Codin

CERTIFICATE OF MAILING

I hereby certify that this Supplemental Information Disclosure Statement, and the documents attached hereto, are being deposited with the United States Postal Service as First Class mail in an envelope addressed to Assistant Commissioner for Patents, Washington, D.C. 20231 on this date.

Date: June 24, 2002

Virginia Silva

SUPPLEMENTAL INFORMATION DISCLOSURE STAT

Assistant Commissioner for Patents Washington, D.C. 20231

Sir:

In compliance with the duty of disclosure under 37 C.F.R. § 1.56, and is accordance with the practice under 37 C.F.R. § 1.97 and § 1.98, the Examiner's attention is directed to the documents listed on the enclosed Form PTO-1449. Copies of the listed information are also enclosed.

This Information Disclosure Statement is being filed within three months of the U.S. filing date or before the mailing date of a first Office Action on the merits. No statement or fee is required (37 CFR §1.97(b)).

CONCLUSION

The Commissioner is hereby authorized to charge any additional fees which may be required, or credit any over-payment to Deposit Account 19-2746.

It is respectfully requested that the above information be considered by the Examiner and that a copy of the enclosed Form PTO-1449 be initialed and returned indicating that such information has been considered.

Respectfully submitted,

Michael T. Selik

Michael T. Gabrik Registration No. 32,896 PECEIVED

JUL 0 3 2002

Technology Center 2600

Please address all correspondence to:

Epson Research and Development, Inc. Intellectual Property Department 150 River Oaks Parkway, Suite 225 San Jose, CA 95134 Customer No. 20178 Phone: (408) 952-6000

Fax: (408) 954-9058 Date: June 24, 2002

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